



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raymond J. Beffa

Serial No.: 10/092,185

Filed: March 6, 2002

For: METHOD FOR SORTING
INTEGRATED CIRCUIT DEVICES

Confirmation No.: 1655

Examiner: J. Rodriguez

Group Art Unit: 3653

Attorney Docket No.: 2269-3037.10US
(95-1074.10/US)

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Date of Deposit with USPS: November 2, 2005

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Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO/SB/08 be considered by the Examiner and made of record. Pursuant to M.P.E.P. 609 III A(2), copies of U.S. patents are not being provided. Copies of foreign patent documents and non-patent literature are enclosed pursuant to 37 C.F.R. § 1.98(a)(2).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
4,027,246	05/1977	Caccoma et al.
4,150,331	04/1979	Lacher
4,454,413	06/1984	Morton, Jr.
4,985,988	01/1991	Littlebury
5,043,657	08/1991	Amazeen et al.
5,118,369	06/1992	Shamir
5,175,774	12/1992	Traux et al.
5,197,650	03/1993	Monzen et al.
5,347,463	09/1994	Nakamura et al.
5,350,715	09/1994	Lee
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5,399,531	03/1995	Wu
5,448,488	09/1995	Oshima
5,654,204	08/1997	Anderson
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5,801,067	09/1998	Shaw et al.
5,805,472	09/1998	Fukasawa
5,837,558	11/1998	Zuniga et al.
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6,613,590 B2	09/2003	Simmons
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Foreign Patent Documents

<u>Document No.</u>	<u>Publication Date</u>	<u>Patentee</u>
JP 02246312 A	10/02/1990	Fujitsu Ltd.
JP 04080949 A	03/13/1992	Hitachi Ltd.
JP 04318911 A	11/10/1992	Matsushita Electric

Serial No. 10/092,185

JP 05121573 A	05/18/1993	Sharp Corp
JP 05315207	11/26/1993	NEC Corp.
JP 06013443 A	01/21/1994	Toshiba Seiku KK
JP 06349691 A	12/22/1994	Hitachi Ltd.
JP 07050233	02/21/1995	Kawasaki Steel
JP 11008327	01/12/1999	Sony Corp
JP 58052814 A	03/29/1983	NEC Corp.

Other Documents

Fresonke, Dean, *In-Fab Identification of Silicon Wafers with Clean, Laser Marked Barcodes*, Advanced Semiconductor Manufacturing Conference and Workshop, 1994, IEEE/SEMI, pp. 157-60.

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application, and therefore no fee is due.

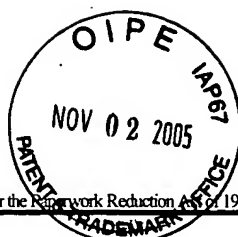
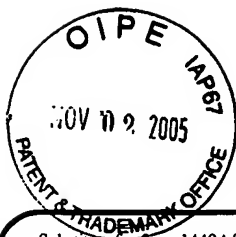
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Date: November 2, 2005
JRD/ps:lmh

Enclosures: Form PTO/SB/08
Copy of non-U.S. Patent documents cited
Document in ProLaw



PTO/SB/08A (10-01)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 3

Complete if Known

Application Number	10/092,185
Filing Date	March 6, 2002
First Named Inventor	Raymond J. Beffa
Group Art Unit	3653
Examiner Name	J. Rodriguez
Attorney Docket Number	2269-3037.10US (95-1074.10/US)

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		4,027,246	05/1977	Caccoma et al.	
		4,150,331	04/1979	Lacher	
		4,454,413	06/1984	Morton, Jr.	
		4,985,988	01/1991	Littlebury	
		5,043,657	08/1991	Amazeen et al.	
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		5,175,774	12/1992	Traux et al.	
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		5,347,463	09/1994	Nakamura et al.	
		5,350,715	09/1994	Lee	
		5,360,747	11/1994	Larson et al.	
		5,399,531	03/1995	Wu	
		5,448,488	09/1995	Oshima	
		5,654,204	08/1997	Anderson	
		5,787,012	07/1998	Levitt	
		5,801,067	09/1998	Shaw et al.	
		5,805,472	09/1998	Fukasawa	
		5,837,558	11/1998	Zuniga et al.	
		5,890,807	04/1999	Ingel et al.	
		5,994,915	11/1999	Farnworth et al.	

FOREIGN PATENT DOCUMENTS

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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		JP 02246312 A	10/02/1990	Fujitsu Ltd.		X
		JP 04080949 A	03/13/1992	Hitachi Ltd.		X
		JP 04318911 A	11/10/1992	Matsushita Electric		X
		JP 05121573 A	05/18/1993	Sharp Corp		X
		JP 05315207	11/26/1993	NEC Corp.		X
		JP 06013443 A	01/21/1994	Toshiba Seiku KK		X
		JP 06349691 A	12/22/1994	Hitachi Ltd.		X
		JP 07050233	02/21/1995	Kawasaki Steel		X
		JP 11008327	01/12/1999	Sony Corp		X

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3

Complete if Known

Application Number

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Filing Date

March 6, 2002

First Named Inventor

Raymond J. Beffa

Group Art Unit

3653

Examiner Name

J. Rodriguez

Attorney Docket Number

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U.S. PATENT DOCUMENTS

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